IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: SHU TAN et al. Serial No: 2432866 Filed: JUNE 20, 2003 Title: **DEBUGGING OPTIMIZED FLOWS**	Group Art Unit: Intellectual Property Law Department International Business Machines Corporation 11400 Burnet Road Austin, Texas 78758

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner of Patents
Washington, D. C. 20231

Sir:

Applicants submit herewith patents, publications or other information of which they are aware, which they believe may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 C.F.R. 1.56.
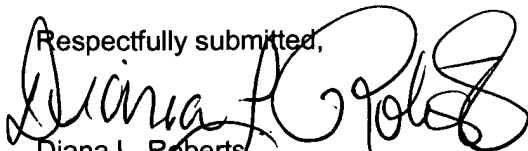
While this Information Disclosure Statement may be "material" pursuant to 37 C.F.R. 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such.

In accordance with 37 C.F.R. 1.97(b) the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56 (a) exists.

The patents and/or publications submitted herewith are set forth on the attached form, PTO-1449.

In the event a fee is required in connection with the enclosed Information Disclosure Statement, the Commissioner of Patents and Trademarks is authorized to charge Deposit Account No. 09-0451 for the necessary amount.

Respectfully submitted,

Diana L. Roberts
Registration No. 36,654
Intellectual Property Law Dept.
IBM Corporation
11400 Burnet Road 4054
Austin, Texas 75758
Telephone: (512) 823-9667

In Plac of FORM PTO-1449 (M dified)	ATTORNEY DOCKET NO. CA920030042US1	SERIAL NO.
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	Applicant: SHU TAN, et al.	
	Filing Date: June 20, 2003	Group:

Reference Designation — U. S. PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Dat If Appropriat
	AA	20030041315A1	02/27/2003	Bates, et al.			
	AB	20030028858A1	02/06/2003	Hines, Kenneth J.			
	AC	20030009548A1	01/09/2003	Poynor, Todd			
	AD	20020188930A1	12/12/2002	Moser, et al.			
	AE	20020120919A1	08/29/2002	Aizenbud-Reshef, et al			
	AF	6427232	07/30/2002	Ku, et al			
	AG	6412106	06/25/2002	Leask, et al.			
	AH	6118448	09/12/2000	McMillian, et al.			
	AI	US20030023955A1	01/30/2003	Bates, et al.			
	AJ	US6324683	11/27/2001	Fuh, et al.			
	AK	US6263489	07/17/2001	Olsen, et al.			
	AL	USRE36852	09/05/2000	Heinen, Jr., Roger J.			

FOREIGN PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Country	Class	Subclass	Translation Yes No

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner Initial		
	AM	Takeshi Ogasawara, "Method and Apparatus for Displaying the Call Stack of Optimized Programs in Diagnostic or Debugging Tools", RD n443 03-2001, Article 132, p. 469
	AN	Tang M. Min, "Enable Debuggers as an Objective Performance Measurement Tool for Software Development Cost Reduction", RD n444 04-2001, Article 188, pp. 686-688
	AO	Besaw, K.; Donnovan, RJ.; "Monotonically Increasing Counter for Local Code Optimization Algorithm", TDB v36 n 12 12-93, pp. 663-668
	AP	"WebLogic Server 6.1 API Reference: Interface JDBCConnectionPoolMBean" BEA System, Inc., 2001
	AQ	Ma, Z.J.; Deng, L.; "A Path-Stack Algorithm for Optimizing Dynamic Regimes in a Statistical Hidden Dynamic Model of Speech.

Examiner: Date Considered:

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.